



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robert Everest Johnson et al.

Art Unit: 2817

Serial No.: 10/679,873

Examiner: Unknown

Filed: For:

October 6, 2003

Atty. Docket No.: ANCO/89US

ARCHITECTURE AND IMPLEMENTATION METHODS OF DIGITAL

PREDISTORTION CIRCUITRY

TRANSMITTAL

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PATENT ANCO-89US/119 Confirmation No. 3837

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INFORMATION DISCLOSURE STATEMENT

In accordance with the duty of candor and good faith imposed by 37 CFR §1.56 and means of complying therewith according to 37 CFR §§1.97 and 1.98, the references listed on the attached forms PTO-1449 are called to the attention of the U.S. Patent and Trademark Office in connection with the above-identified application. Copies of the references are enclosed.

Applicants also note that this application may be related to the following co-pending U.S. Patent Applications:

Serial No.	Filing Date	<u>Inventor</u>
09/395,490	September 14, 1999	
10/068,343	February 5, 2002	Vella-Coleiro
10/091,756	March 6, 2002	White et al.
10/342,633	January 15, 2003	DeBruyn et al.
10/406,959	April 3, 2003	Leffel
10/625,761	July 23, 2003	Bachman, II et al.

To the best of Applicants' knowledge and belief, this Information Disclosure Statement is being filed before the receipt of a first Office Action on the merits of this application, and therefore, no fees are believed to be due.

The Examiner is respectfully requested to review each reference cited and to note the review on the attached PTO-1449 forms (3 pages) and to reach their own independent determination of its materiality, if any. It should also be noted that Applicants are filing on even date herewith two Electronic Information Disclosure Statements with a total of 80 U.S. patent references listed therein.

If any charges or credits are necessary to complete this communication, please apply them to Deposit Account 23-3000.

Respectfully submitted,

Kurt A. Summe

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Sheet 1 (A) of 3 SUBSTITUTE FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE ATTY, DOCKET NO. SERIAL NO. PATENT AND TRADEMARK OFFICE ANCO/89US 10/679,873 APPLICANT INFORMATION DISCLOSURE Confirmation No. STATEMENT BY APPLICANT Robert Everest Johnson et al. 3837 (Use several sheets if necessary) **FILING DATE GROUP** October 6, 2003 2817 (37 CFR 1.98(b) **U.S. PATENT DOCUMENTS** FILING DATE **EXAMINER** ISSUE IF APPROPRIATE PATENT NUMBER **CLASS SUBCLASS** INITIAL DATE **PATENTEE** A.A A.B A.C A.D A.E A.F A.G A.H A.I A.J A.K FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS DOCUMENT **PUBLICATION** COUNTRY OR --**TRANSLATION** NUMBER DATE PATENT OFFICE **CLASS SUBCLASS** (YES/NO) **EPO** A.L EP 0 367 457 B1 27.03.1996 H03F 1/32 Yes 27.03.2002 **EPO** H03F 1/32 Yes A.M EP 0 948 131 B1 EP 0 998 026 A1 03.05.2000 **EPO** H03F 1/32 A.N Yes WO 97/08822 **WIPO** H03F Yes A.O 06.03.1997 1/32 A.P A.Q OTHER DOCUMENTS (Including Author, Title, Date, Place of Publication) A.R Lohtia, Anit et al., "Power Amplifier Linearization using Cubic Spline Interpolation", IEEE, (1993), No. 0-7803-1266-x/93, pp. 676-679. A.S Stapleton, Shawn P., "Amplifier Linearization Using Adaptive Digital Predistortion - The need for greater linearity can be addressed at the digital coding level", Applied Microwave & Wireless, Technical Feature, (February 2001), pp.72-77. Vella-Coleiro, George, Frequency-Dependent Phase Pre-Distortion for Reducing Spurious Emissions in Communication A.T Networks, U.S. Patent Application Serial No. 10/068,343, filed on February 5, 2002.

EXAMINER: Initial if citation considered, whether or not in conformance. Draw line through citation only if not in conformance <u>and</u> not considered. Include a copy of this form with next communication to applicant.

DATE CONSIDERED

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	B.S	DeBruyn, William P. et al., An Uncorrelated Adaptive Predistorter, U.S. Patent Application Serial No. 10/342,633, filed on January 15, 2003.																

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EXAMINER

Leffel, Michael D., Independence Between Paths That Predistort for Memory and Memory-less Distortion in Power Amplifiers, U.S. Patent Application Serial No. 10/406,959, filed on April 3, 2003.

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	C.R	Bachman, II et al., Elimination of Peak Clipping and Improved Efficiency for RF Power Amplifiers with a Predistorter, U.S. Patent Application Serial No. 10/625,761, filed on July 23, 2003.											Predistorter, U.S.			
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